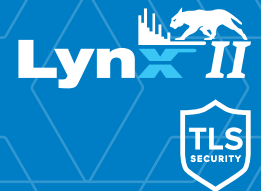




SIGNAL PROCESSING

Lynx[®] II

Digital Signal Analyzer

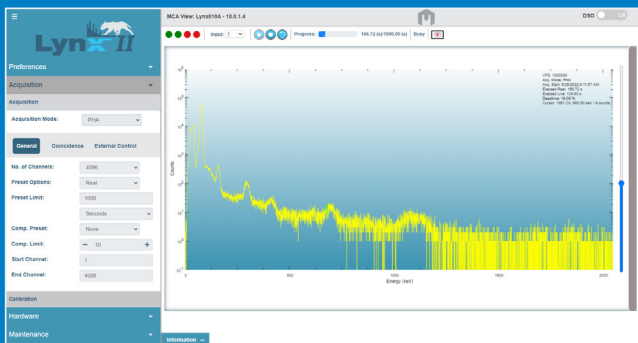


FEATURES

- ✓ Front panel LCD
- ✓ Cybersecurity features built on a Linux platform
- ✓ Integrated rack mountable MCA based on Digital Signal Processing (DSP) technology
- ✓ Two groups of 32k channel conversion gain/spectral memory
- ✓ Operates in pulse height analysis (PHA), multichannel scaling (MCS), dual channel loss free counting (LFC), multispectral scaling (MSS) and time-stamped list modes
- ✓ Superior count rate and temperature stability
- ✓ Flexible range of processing time parameters for precise match to application requirements
- ✓ 10/100/1000 Base-T Ethernet and USB On-the-Go (OTG) for fast, convenient industry-standard communications
- ✓ Full complement of control and signal input/output interfaces
- ✓ Three HVPS ranges to support detectors with low, medium or high bias requirements
- ✓ Advanced auto pole/zero US Patent #7725281, baseline restoration and digital stabilization capability
- ✓ Adjustable digital signal delay, input signal delay, and coincidence window for advanced coincidence timing applications
- ✓ Advanced Coincidence counting modes store both original full spectrum and gated spectrum for complete traceability and data control
- ✓ Multiple trace analog and digital oscilloscope to ensure easy setup and maintenance
- ✓ Full-featured support through Genie™ 2000/ Apex® spectroscopy software
- ✓ Optional software development kit with examples
- ✓ Password-protected built-in web server interface allows easy and remote access to spectral display and status, acquisition control, instrument setup, and diagnostics

LYNX II DIGITAL SIGNAL ANALYZER

The Lynx II DSA is the most advanced, full-featured Multichannel Analyzer ever offered. It is a 32k channel integrated signal analyzer based on advanced digital signal processing (DSP) techniques. When paired with the computer of choice, the Lynx II unit becomes a complete spectroscopy workstation capable of the highest quality acquisition and analysis. The instrument supports a wide range of spectroscopy detector technologies such as HPGe, Si(Li), PIPS®, X-PIPS™, CdTe, Cd(Zn)Te applications and virtually all scintillation detectors used for gamma spectroscopy. It also supports the unique energy ranges of all these detectors from 1 keV and up.



This highly integrated instrument incorporates front-end signal conditioning, a fast digitizing analog-to-digital converter (ADC), programmable digital filters, digital oscilloscope, automatic pole/zero US Patent #7725281 and baseline restorer, digital fast discriminator, two groups of 32k channel spectral memory, digital stabilizer and a triple-range HVPS – all in a small, compact package. The front panel LCD and rear panel lighted indicators alert the user to power, acquisition, communication, high voltage and count rate status.

The Lynx II instrument is operated through the Mirion Genie 2000 and Apex spectroscopy software which provides the user with an extensive selection of application specific software options. The Lynx II unit may also be operated via the Internet or a Local Area Network (LAN) without any spectroscopy software at all. Only a web browser is required for data acquisition display and control, system energy/shape calibration and operation of the digital oscilloscope from a Microsoft Windows, macOS or Linux computer. The web browser capability can be used for controlling and checking count status from any computer on the Internet if permission is granted by the system administrator. However, more involved data analysis such as nuclide identification, activity calculation, etc., requires Genie 2000 or Apex Gamma Spectroscopy Software. An OS independent software development kit (SDK) with examples is available and can be used without Genie 2000 software. The SDK is similar to the comprehensive set of programming tools available with the Mirion Osprey® Digital Tube Base MCA. It allows expert users to develop platform independent applications for instrument control and data acquisition.

Lynx II offers improved cybersecurity, taking regulatory requirements and security measures into account, all software technologies within the firmware have been scanned using a suite of vulnerability tools. Firewalls are used to prevent unauthorized access. Transport Layer Security authenticates and encrypts communication interfaces. The MCA has a Trusted Execution Environment (TEE) that ensures the integrity and authenticity of all firmware components (bootloaders, kernel, filesystems, trusted applications, etc.).

With the Lynx II DSA, acquisition can be configured for pulse height analysis (PHA) or multichannel scaling (MCS) modes supporting two groups of up to 32k channels. Simultaneous PHA and MCS data acquisition is available as well. A multispectral scaling (MSS) acquisition mode, sometimes referred to as “ping-pong” mode, is supported for rapidly collecting large numbers of PHA spectra with little-to-no data loss between acquisitions. Time-stamped list mode is also supported and will record the time and energy of each individual energy event as it occurs during a count. Energy and time-correlated spectra can then be created from the data. Time series data collection is possible for up to eight Single Channel Analyzers (SCAs) and two auxiliary counters. The LYNX-SDK software development kit programming libraries are required for operation in MSS and TLIST modes. SCAs, auxiliary counters, and GPIOs also require the LYNX-SDK, however; all data collection methods can be setup using a web browser.

LYNX II DIGITAL SIGNAL ANALYZER



The Lynx II DSA is designed to do much more than just count samples in the lab. It includes a complete array of input/output control and signal connections on the rear panel – many provide features you would typically have to pay extra to receive if you could get them at all. With the Lynx II unit, they are all included as BNC connections for: TRP Inhibit, HV Inhibit, MCS In. External PHA and MCS Start/Stop, External MCS Channel and Sweep Advance, Coincidence/Anticoincidence Gating, Sample Changer control, SCA and Fast Discriminator out, Synchronization control of multiple Lynx II units, Monitor Out, plus three General Purpose I/O connectors for future or custom applications. These remaining signals are MCX connections located under an advanced function plate.

The Lynx II unit has a built-in Digital and Analog Oscilloscope (shown above) accessible via the web browser interface which provides assistance with instrument set up, pole/zero optimization and manual reset preamp inhibit adjustments. The operator has the ability to view any one of six analog signals and any or all of eight digital signals on the oscilloscope display at the same time. Scaling and trigger functions are similar to those of an actual oscilloscope, and the displayed traces can be printed or saved to disk with scale information and recalled at a later date. Set up and optimization has never been easier!

The Lynx II DSA also offers superior coincidence gating functionality and performance. When performing a coincidence measurement, the primary detector pulse is either stored or rejected (Coincidence or Anti-coincidence modes respectively), based on the presence of an external gate signal being present when the primary detector Store pulse occurs. Typically, the external gate is another detector, such as in a Cosmic veto or Compton suppression system. The integrated digital oscilloscope can be used to align the external gating pulse with the primary detector Store pulse directly. The user can control both the width of the external gate and the relative position of the external gate with respect to the primary detector Store pulse using either the built-in oscilloscope or Genie 2000 software. In addition, the ungated spectrum is automatically saved in the second memory group, with the Combined Advanced Coincidence/Anti-Coincidence modes only, allowing for easy comparison with the gated spectrum. This allows for complete record and traceability of the original unaltered spectrum and provides a convenient mechanism for assessing the system performance.



The Lynx II DSA is rack mountable, but is completely enclosed and can be placed on a desktop as well. An optional Rackmount Kit is available for mounting two Lynx II units side-by-side in two units (3.5 in. of height) in a standard 19-inch electronics cabinet or desk pedestal. The kit includes mounting hardware, rack handles and a spacer that takes the place of one Lynx II unit if only one is being installed in the cabinet. Of course, the blank can easily be replaced with a second Lynx II unit at any time to double the counting capacity without requiring any additional vertical space in the cabinet.

LYNX II DIGITAL SIGNAL ANALYZER

INPUTS (REAR PANEL)

ENERGY IN

- Detector/preamplifier signal input connector, BNC. Accepts positive or negative signals from an associated detector preamplifier; amplitude for full scale conversion ± 1 V divided by selected gain; maximum input (signal +DC) for linear operation is dependent on the Input Attenuator setting; Attenuator OFF (x1): ± 4 V, Attenuator ON (x 0.25): ± 12 V, DC coupled and protected to ± 12 V maximum; rise time: less than the selected Rise Time + Flat Top settings; acceptable preamplifier decay time constant: 40 μ s to infinity; Z_{in} is 1k.

TRP INH

- Reset-Preamplifier inhibit input connector, BNC.
- TTL compatible logic signal. Software selectable polarity, functionality depends on the mode selected.
 - Norm: Signal inhibits the signal processing and extends the dead time for the duration of the inhibit signal.
 - Reset: Signal inhibits the signal processing and extends the dead time for the duration of the inhibit signal OR the duration of an internal inhibit timer. Inhibit time is the longer of these two inhibit sources.

HV INH

- Preamplifier high voltage inhibit input connector, BNC.
- Accepts input from the detector preamplifier to shut down the HVPS in the event of a detector warm-up above its safe operating temperature; polarity is software selectable to match the preamplifier.
 - Positive polarity: (for Mirion preamplifiers); Enable condition (cold detector) is an open circuit or active high = +1.2 V to +24 V; Inhibit condition (warm detector) is -24 V to <+1.2 V or ground.
 - Negative polarity: for preamplifiers and LN monitors where enable condition (cold detector) is -24 V to <+1.2 V; Inhibit condition (warm detector) is open circuit or active high = +1.2 V to +24 V. With Negative polarity selected an open input will disable the high voltage.

MCS IN

- MCS input connector, BNC. TTL compatible. Minimum pulse width is 25 ns.

PHA ACQUIRE START/STOP

- External PHA Start/Stop input connector, MCX. TTL compatible, software selectable polarity.
- Acquisition must first be armed. Once armed, the external start / stop control setting dictates the behavior of the acquisition.
- Pulse mode and Level mode are supported. Mode is programmable via software.
 - Pulse Mode includes Start, Stop, and Start and Stop functions. Functions are triggered by pulses > 25 ns.
 - Level Mode includes Suspend / Resume. Active signal will resume acquisition; inactive signal will suspend acquisition.

MCS ACQUIRE START/STOP

- External MCS Start/Stop input connector, MCX. TTL compatible, software selectable polarity.
- Acquisition must first be armed. Once armed, the external start / stop control setting dictates the behavior of the acquisition.
- Pulse mode and Level mode are supported. Mode is programmable via software.
 - Pulse Mode includes Start, Stop, and Start and Stop functions. Functions are triggered by pulses > 25 ns.
 - Level Mode includes Suspend / Resume. Active signal will resume acquisition; inactive signal will suspend acquisition.

MCS CHANNEL ADVANCE

- External MCS Channel Advance input connector, MCX.
- TTL compatible, Software selectable polarity.
- Pulse >25 ns advances the current MCS channel.

MCS SWEEP ADVANCE

- External MCS Sweep Advance input connector, MCX.
- TTL compatible, software selectable polarity. Pulse >25 ns resets the current sweep in progress. The next sweep will begin. This will occur with each pulse until the preset sweeps is reached.

GATE – COINCIDENCE ANTICOINCIDENCE LOGIC MODES

- Coincidence/anti-coincidence input connector, MCX.
- TTL compatible; software polarity selectable.
- Legacy Coincidence/anti-coincidence modes: Signal active at the pulse processing time allows storage/blocks storage of the event.
- Combined advanced coincidence/anti coincidence: Storage of an event occurs (coincidence gating) or is blocked (anti-coincidence gating) when the event's pulse processing/storage time occurs within the storage window. Simultaneous storage of both non-gated and gated events in Lynx II memory groups.
- User adjustable storage window by user adjustable Gate Delay, Input Gate Delay and Input Pulse Width parameters.
- The pulse processing time occurs at 1.1x (rise time + flat top).

CHANGER READY

- Sample Changer Ready input connector, MCX.
- TTL compatible, software selectable polarity.
- Active signal allows acquisition. Inactive signal will delay the start of acquisition.

SYNC

- Synchronization input/output signal connector, MCX. For multiple Lynx II units running concurrent acquisitions.
- TTL compatible; Signal operation and direction are software selectable as Master or Slave.
 - PHA mode: The Master produces a sync signal at the programmed rate. The Slave synchronizes the acquisition to the sync signal.
 - List mode: Master instrument: Signal is an output and provides an output pulse every time the DSA internal 1 μ s time base timer overflows. Pulse width >100 ns. Slave mode: Signal is an input. Each time the signal pulses >25 ns, the 1 μ s internal time base timer will reset.

LYNX II DIGITAL SIGNAL ANALYZER

GENERAL PURPOSE INPUTS/OUTPUTS – GP I/O PORTS

- Buffered input/output signal connectors, three MCX's. For connections to processing electronics with TTL compatible signals.
- GP I/O ports 1 and 2 are configured as counter inputs for external TTL signals. Minimum pulse width is 25 ns; maximum input rate is 20 MHz.
- The third GP I/O port is available for future use.
- GP I/O 3 is ACQ Out to be used for Lamp Driver

DC POWER IN

- 12 V (10-15 V) dc power jack (mechanically lockable to prevent accidental disconnection).
- Current 2.0 A max controlled by rear panel power switch.

OUTPUTS (REAR PANEL)

MONITOR OUT

- Shaper monitor output connector, MCX. This signal is the reconstructed energy shaper signal, and it may be observed by connecting it to an external oscilloscope. The signal range is ± 2 V.

ICR (INCOMING COUNT RATE)

- Fast discriminator output connector, MCX. TTL compatible. Produces a positive pulse 100 ns wide for each incoming event above the instrument's threshold setting.

CHANGER ADV

- Sample Changer Advance output connector, MCX.
- TTL compatible; Software selectable polarity.
- Pulses for ~ 150 ms for each sample advance command the instrument receives.

SCA

- Single Channel Analyzer output connector, MCX.
- Pulses for each event with an energy between the lower and upper level discriminators.

PREAMP POWER

- Preamp power connector, 9-pin female D-connector. Provides ± 12 V ($\pm 3\%$), ± 24 V ($\pm 3\%$) and ground for standard preamplifiers; overload protected.
 - +12 V @ 100 mA
 - -12 V @ 100 mA
 - +24 V @ 50 mA
 - -24 V @ 50 mA
- Includes Pin for NAID™ Detector Thermistor Monitor In.

HV+

- Positive High Voltage output connector, SHV.
- Software selectable range of 150 – 1500 V dc or 1500 – 5000 V dc.

HV-

- Negative High Voltage output connector, SHV.
- Software selectable negative ranges of 150 – 1500 V dc or 1500 – 5000 V dc.

+/-200 V

- ± 200 V dc voltage output connector, SHV.
Note: For the three SHV connectors (Positive, Negative and ± 200 V output), only a single SHV connector can be used at any one time.

COMMUNICATION PORTS (REAR PANEL)

10/100/1000 BASE-T ETHERNET

- Female RJ-45 connector for connection to an Ethernet network or directly to a host computer for all data acquisition and application set-up needs. If an Ethernet switch is required, a "Gigabit" type switch must be used.

USB OTG

- Micro USB connector. USB support for connection to host computer or USB hub for acquisition, setup and diagnostic purposes.
- Two USB A connectors - For future use.

FRONT PANEL DISPLAY

FRONT PANEL LIQUID CRYSTAL DISPLAY (LCD)

Displaying:

- MCA name
- USB address
- IP address
- Mac address
- Acquisition mode and status
- Incoming count rate
- Dead time
- High Voltage level and status
- Current date/time including selectable time zone
- Installed firmware version

LYNX II DIGITAL SIGNAL ANALYZER

PROGRAMMABLE CONTROLS

GAIN

The combination of Coarse Gain and Fine Gain set the overall system gain to match the requirements of the detector and energy range for the application; overall gain is continuously adjustable from x1.6 to x516.3 based on a 2 V full scale. Upper gain adjustment is comparable to a x2581.5 setting on a traditional analog amplifier with 10 V full scale.

- Coarse gain: Range is x2 to x430 in 19% increments.
- Fine gain: Range is x0.8 to x1.2 in ~0.001% increments.
- Gain attenuator: ON/OFF; When ON (selected) enables a divide by four input attenuator to minimize overload due to preamp signals with large DC offsets and Reset Preamps with large output ramp dynamic range. When OFF is selected the signal attenuation is removed.
- Conversion gain: Selections of 256, 512, 1024, 2048, 4096, 8192, 16384 or 32768 channels. Support for two memory groups of 32768 or less channels.
- LLD mode: Selects Automatic or Manual Lower Level Discriminator (LLD) mode; AUTOMATIC: the LLD cutoff is automatically set just above the spectral noise threshold; MANUAL: allows the LLD cutoff to be set manually as a percentage of the full scale spectral size or range.
- LLD setting: Active when the Manual LLD mode is selected, sets the minimum input acceptance level, range is 0 to 100%.
- ULD setting: Sets the maximum input acceptance level for the Upper Level Discriminator (ULD), range is 0 to 110%.
- Input polarity: Selects either POSITIVE or NEGATIVE input polarity.
- TRP inhibit mode: Selects AUTO or MANUAL Reset Preamp Inhibit Modes; disables pulse processing, extends the system dead time, reinitializes the pileup rejector and gates off the baseline restorer. AUTO: System is gated off for the greater of the external RESET signal "OR" the Internal Inhibit Time. MANUAL: Functionality same as Auto mode except the signal processor is inhibited for the greater of the user selected Inhibit Setting "OR" the external RESET signal "OR" the Internal Inhibit Time.
- TRP inhibit setting: Active when the MANUAL Reset Preamp Inhibit Mode is selected, sets the Inhibit Time, range 0 to 160 μ s in increments of 10 μ s.
- TRP gate polarity setting: Sets the TRP input polarity to POSITIVE or NEGATIVE.

DIGITAL FILTER

Note: Filter output (Trapezoid Signal) may be displayed on the Host computer using the digital oscilloscope feature.

- Rise time: 255 settings for rise and fall times from 0.2 to 51 μ s in 0.2 μ s increments.
- Flat top: 33 settings for flat top times from 0 to 3.2 μ s in 0.1 μ s increments.
- PUR guard: Selects Guard Time (GT) multiplier from 1.1 to 2.5 in increments of 0.1 to reject trailing edge pile-up in the event of detector/preamp anomalies.
- Fast disc shaping: Selects NORMAL or LOW ENERGY to optimize the fast discriminator shaping for the selected detector type; NORMAL: The Fast Discriminator shaping is optimized for Ge detectors and general gamma spectroscopy; LOW ENERGY: The Fast Discriminator filter rise time is set proportional to the slow shaping rise time selection.
- Fast disc mode: Sets the Fast Discriminator Threshold mode. AUTO: the threshold is optimized automatically above the system noise level; MANUAL: allows threshold to be adjusted manually.
- Fast disc setting: Active when manual FAST DISC MODE is selected; sets the Fast Discriminator threshold level, range is 0 to 100%; the rear panel ICR LED serves as a user aid when manually setting the Fast Discriminator threshold.
- PUR/LTC mode: ON/OFF; ON: Enables pileup rejector and live time corrector (LTC). LTC generates dead time to extend the acquisition time to compensate for events that are piled up and rejected; OFF: pileup rejector and LTC disabled.
- LT trim: Allows adjustment of the trapezoidal pulse evolution time or dead time to optimize Live Time Correction (LTC) performance. The adjustment range is 0 to 1000; the default value of 500 provides good LTC performance for a wide range of applications.
- BLR mode: AUTO, HARD, MEDIUM, SOFT; AUTO: The baseline restorer is automatically optimized as a function of trapezoid shaping time and count rate; HARD, MEDIUM, or SOFT: Sets the baseline restorer to fixed rates as selected.
- Auto pole/zero: Pole/zero is adjusted by computer control; range: 40 μ s to infinity; a digital oscilloscope is provided as a user aid when optimizing the pole/zero setting. The oscilloscope measures and analyzes the tail of the trapezoid signal and provides visual feedback showing the quality of the pole/zero adjustment.
- Preamp type: RC, TRP (RESET); selects the pole/zero mode; RC: pole/zero can be adjusted manually by computer command; range: 40 μ s to infinity; TRP (RESET): Sets pole/zero at infinity for use with pulsed charged restoration (RESET or TRP) preamplifiers.

MIXED SIGNAL OSCILLOSCOPE

- Allows examination of the digital signal reconstructed in time to assist and verify instrument setup, pole/zero optimization and manual reset preamp inhibit adjustments. The operator has the ability to view any one of six analog signals on the same screen as any or all of eight digital signals.

LYNX II DIGITAL SIGNAL ANALYZER

STABILIZER

- Gain mode: ON, OFF, HOLD; ON/OFF: enables or disables the Gain Mode; HOLD: disables the stabilizer Gain Mode, but maintains the current Gain correction factor; Centroid (10 to 32752 channels), Window (1 to 128 channels), Spacing (2 to 512 channels), Ratio (0.01 to 100), Correction Divider (1 to 512 in binary steps); Correction Range is selectable at 1% for Ge and 10% for NaI detectors.

SAMPLE CHANGER CONTROL

- Sample changer advance and ready signal polarities may be set separately for positive and negative. The changer may be manually advanced via an ADVANCE CHANGER button in the software.

SYNCHRONIZATION

- External SYNC status: Disables or Enables the synchronization of multiple Lynx II units.
- External SYNC mode: Sets a particular Lynx II DSA as MASTER to control other units or as a SLAVE to be controlled by another Lynx unit.
- External SYNC pulse/timeout: Separately sets the duration of a SYNC pulse and the timeout if no pulse is received in the selected amount of time, settings in μ s.
- Ext SYNC polarity: Sets the polarity of the SYNC signal as positive or negative.

PERFORMANCE

SIGNAL PROCESSING

- Spectrum broadening: The FWHM of ^{60}Co 1.33 MeV gamma peak for an incoming count rate of 2 kcps to 100 kcps will typically change less than 6% for 2.8 μ s rise/fall time, 0.8 μ s flat top and proper P/Z matching. These results may not be reproducible if the associated detector exhibits an inordinate amount of long rise time signals.
- Integral nonlinearity: $<\pm 0.025\%$ of full scale over the top 99% of selected range.
- Differential nonlinearity: $<\pm 1\%$ over the top 99% of the range including the effects from integral nonlinearity.
- Gain drift: <35 ppm/ $^{\circ}\text{C}$ after 15 minutes of operation.
- Zero drift: <3 ppm/ $^{\circ}\text{C}$ after 15 minutes of operation. Typically, less than 1 channel over full temperature range (8K Spectrum).
- Overload recovery: Recovers to within 1% of full scale output from x1000 overload in 2.5 non overlapped pulse widths at full gain, at any shaping (processing time), and with pole/zero properly set.

PILEUP REJECTION/LIVE TIME CORRECTION

- Pulse pair resolution: Better than 500 ns with NORM Detector Type selected.
- Dead time correction: Extended live time correction, accuracy of reference peak area changes 5% (3% typical) at up to 50% system dead time with a setting of 5.6 μ s rise time and 0.8 μ s flat

DATA ACQUISITION MODES

All device settings are retained during power down until power is restored. Spectral data retention is for a minimum of five years in the event of power loss.

PHA MODE

- Channels: Selections of 256, 512, 1024, 2048, 4096, 8192, 16384, or 32768 channels. Support for two memory groups of 32768 or less channels.
- Digital offset (zero to full scale): Specifies a number of channels to offset the data. The offset shifts the memory assignments of the device's conversion to the left (e.g. an offset of 4096 would shift channel 4096 down to correspond with channel zero of the memory).
- Preset: Real or Live Time timers with range of 0.01 s to 11930 hr with 0.01 s resolution.
- Control: Internal or External Start/Stop control.
- Digital delay: An adjustable digital signal delay for timing applications (coincidence counting) is supported. This feature is used in place of a delay amplifier and supports a wide range of delay times (0 to 160 μ s in 0.2 μ s steps).

DUAL LFC MODE

- Two memory groups of equal size are available for PHA storage. The corrected spectrum is stored in the first group and the uncorrected spectrum in the second group.
- The time indication for each group is as follows:
- The time for the corrected spectrum (group 1) indicates the real measurement time value for both Real and Live timers.
- The time for the uncorrected spectrum (group 2) indicates the system live value (system not busy) for the Live Timer and the real measurement time for the Real Timer.

MCS MODE

- Input source: ROI, Fast Discriminator, or TTL IN.
- Channels: Selections of 256, 512, 1024, 2048, 4096, 8192, 16384, or 32768 channels. Support for two memory groups of 32768 or less channels.
- Dwell time: 1 to 999 in units of μ s, ms or s. Minimum dwell time is 2 μ s; maximum dwell time is 999 s.
- Preset sweep counter: 0 to $2^{32}-1$ sweeps.
- Control: Internal or External Start/Stop control.

TIME STAMPED LIST MODE

- ADC events stored sequentially in the buffer memory. The time of arrival of each event is tagged with the current value of a 1 μ s time counter. Maximum throughput is 100k events per second.

TIME SERIES MODE

- 8 Single Channel Analyzers may be defined and collected independent of the main acquisition mode. The time preset may be as small as 10 milliseconds. Similarly, 2 auxiliary counters may be used to collect pulses on the GPIOs at a sample rate as small as 10 milliseconds.

LYNX II DIGITAL SIGNAL ANALYZER

MULTISPECTRUM SCALING (MSS) MODE

- If configured for MSS data acquisition, the Lynx II unit acquires data into its two PHA memory groups in a “ping-pong” fashion – acquiring alternately into one group while the other group is stored to computer and cleared. If a group has not finished clearing when it is time to switch groups, acquisition continues in the first group until the second group is ready to receive data.
- Minimum count time with no data loss between acquisitions is 100 ms limited by the host computer’s ability to process and store the data such that it can be cleared from Lynx II memory and the memory reused for the next acquisition.

HIGH VOLTAGE POWER SUPPLIES

- All outputs are current limited and short circuit protected.
- HV Inhibit input, two modes:
 - Positive polarity: (for Mirion preamplifiers); Enable condition (cold detector) is an open circuit or active high = +1.2 V to +24 V; Inhibit condition (warm detector) is –24 V to <+1.2 V or ground.
 - Negative polarity: for preamplifiers and LN monitors where enable condition (cold detector) is –24 V to <+1.2 V; Inhibit condition (warm detector) is open circuit or active high = +1.2 V to +24 V. With Negative polarity selected an open input will disable the high voltage.
- Note: After Inhibit is removed the high voltage remains inhibited until the user acknowledges the fault condition via a reset of the high voltage supply.
- Separate SHV connectors for positive and negative high range outputs.
- Single SHV connector for ± 200 V output.
- HVPS Range 1: $\pm 200 - 1500$ V @ 1 mA max.
 - Ripple: 5 mV P-P.
 - Temperature coefficient: ± 50 ppm/ $^{\circ}$ C.
 - Stability: 0.01%/h, 0.02%/8 h.
 - Accuracy: $\pm 5\%$. $\pm 2.5\%$ Typical.
 - Load regulation: 1%.
 - Setting resolution: 12-bit (1/4096).
- HVPS Range 2: $\pm 1500 - 5000$ V @ 1 μ A max.
 - Ripple: 10 mV P-P.
 - Temperature coefficient: ± 50 ppm/ $^{\circ}$ C.
 - Stability: 0.01%/h, 0.02%/8 h.
 - Accuracy: $\pm 5\%$. $\pm 2.5\%$ Typical.
 - Load regulation: 1%.
 - Setting resolution: 12-bit (1/4096).
- HVPS Range 3: ± 200 V @ 100 nA max.
 - Ripple: 10 mV P-P.
 - Temperature coefficient: ± 50 ppm/ $^{\circ}$ C.
 - Stability: 0.01%/h, 0.02%/8 h.
 - Accuracy: $\pm 5\% \pm 5$ V. $\pm 2.5\% \pm 3$ V Typical.
 - Load regulation: 1%.
 - Setting resolution: 12-bit (1/4096).

CHARACTERISTICS

PHYSICAL

- Metal enclosure.
- Size: Half a 19-in. electronics cabinet wide by 2U high – 8.1 x 21.3 x 28.4 cm (3.3 x 8.4 x 11.2 in.) (H x W x D).
- Weight: 2.36 kg (5.2 lb).
- System components:
 - Lynx II Digital Signal Analyzer.
 - Operator’s manual.
 - 3 m (10 ft) CAT 5 Straight-thru UTP Ethernet cable.
 - 1.83 m (6 ft) USB A Male/Micro B Male.
 - Three BNC to MCX coaxial cables, 4 ft.

ENVIRONMENTAL

- Operating temperature range: 0 to 50 $^{\circ}$ C.
- Humidity: Up to 80% non-condensing.

ORDERING INFORMATION

- LYNX-II – Digital Signal Analyzer.
- Compatible with Genie 2000 V3.4 or later.

OPTIONS

- LYNX-SDK – OS independent Software Development Kit.
- LYNX-II-MOUNTKIT.
 - Includes mounting hardware to allow mounting of one or two Lynx II units in a cabinet.



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